ref clk

feedback clk

PLL

global clocking

I2clk Grid

local clocking

I1clk

Clock Distribution Scheme

l2clk grid

l2clk drivers

Metal-8

Y-pitch

Metal-7

X-pitch

RC Delay = Clock Skew

RC/2 Delay = Clock Skew

Connections
to Grid

**Grid Driven by Drivers without
Wire-Tree**

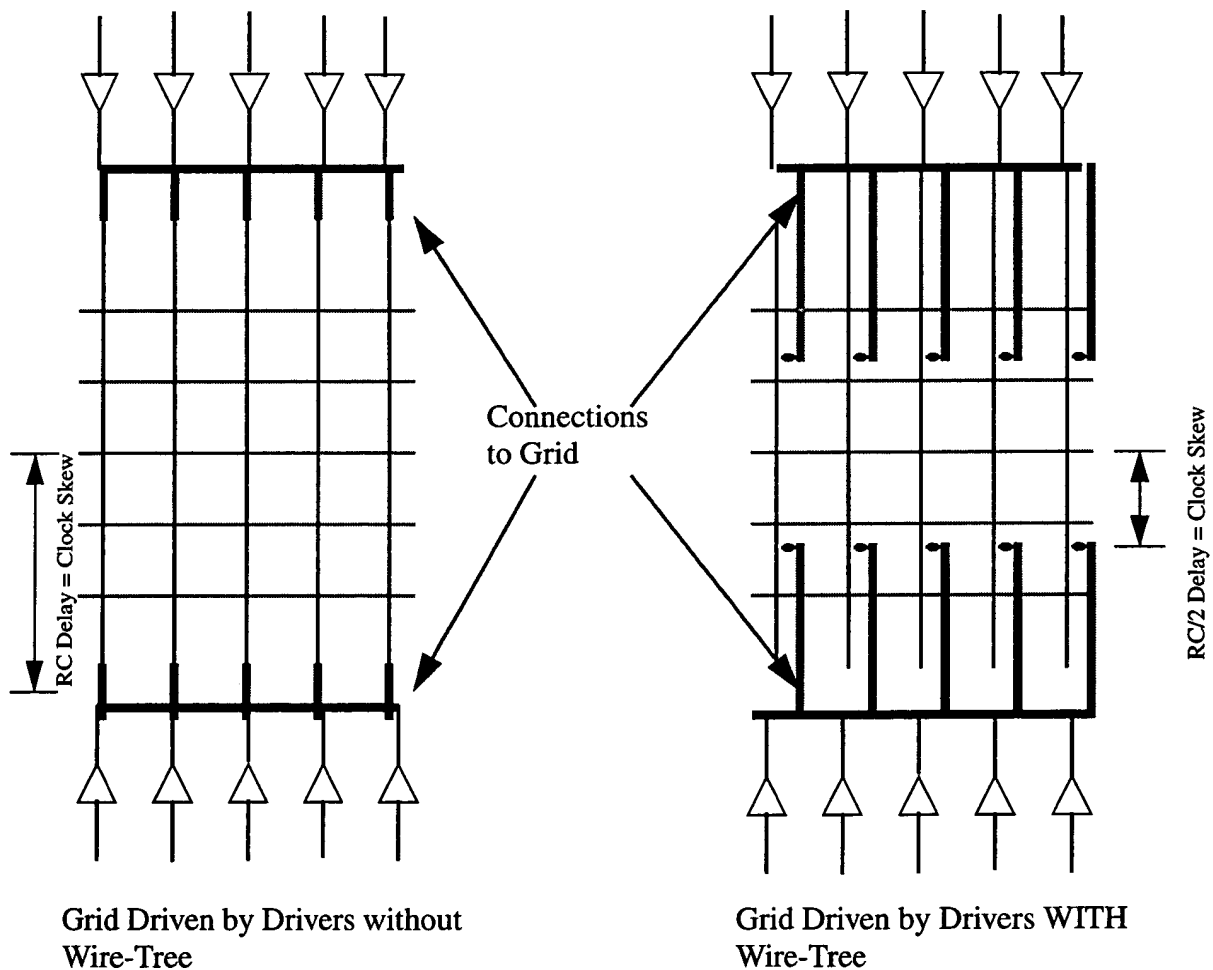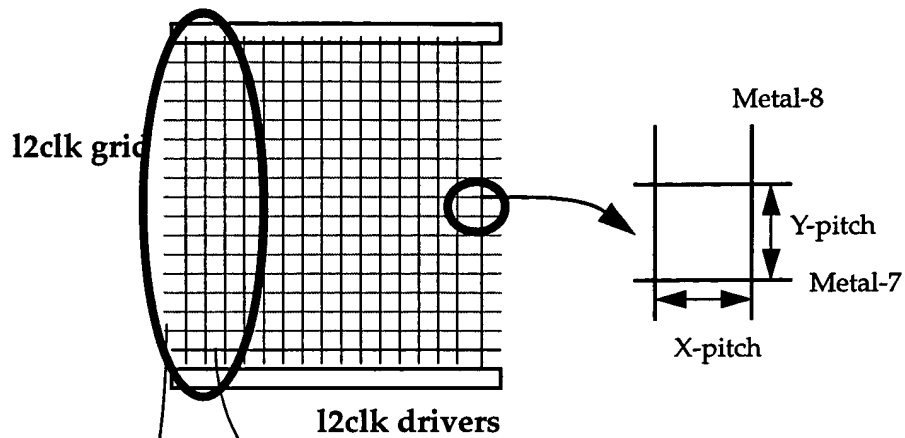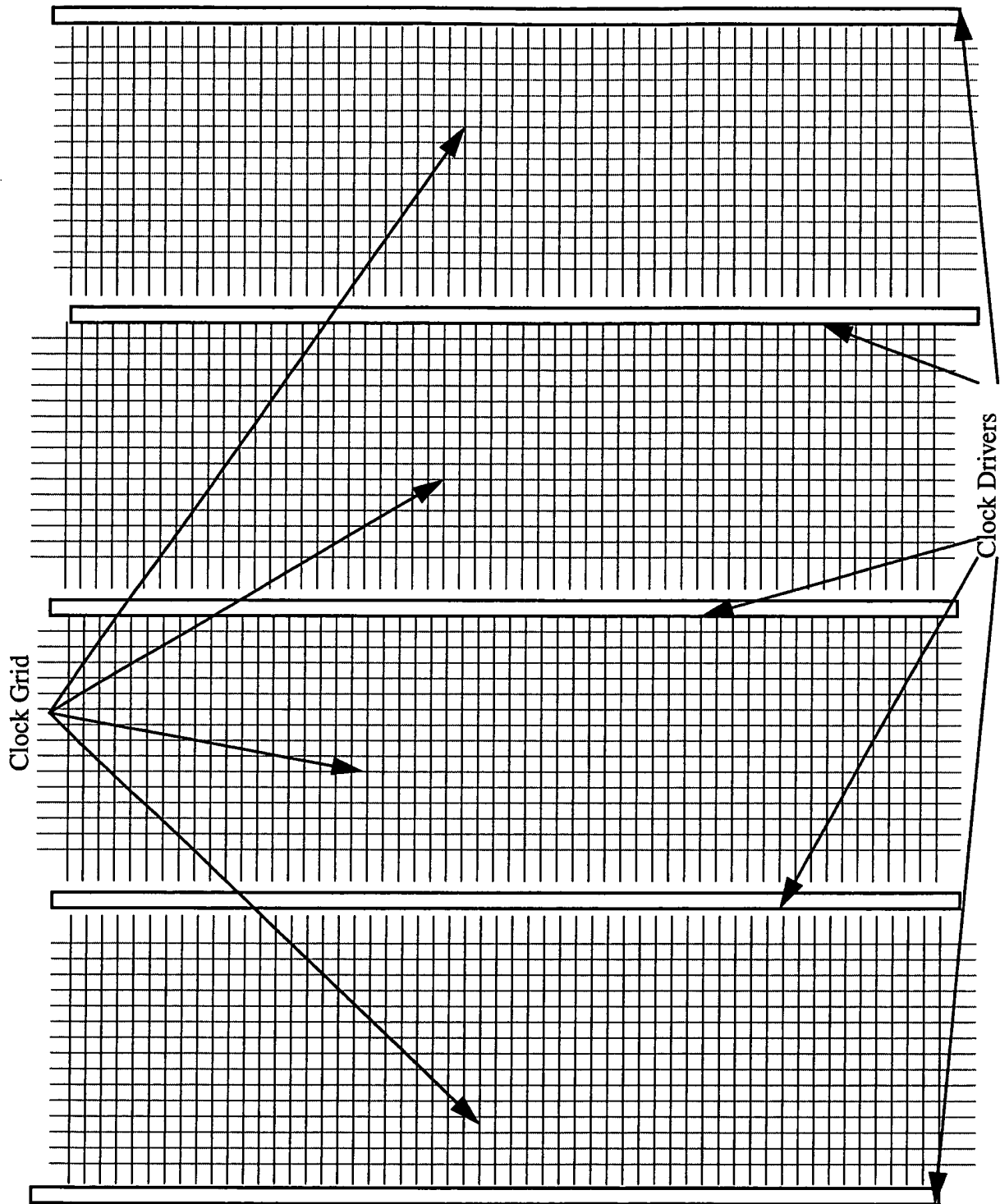**Grid Driven by Drivers WITH
Wire-Tree**

Clock Grid

Clock Drivers

Typical Clock Grid and Clock Driver Arrangement for a